



O-1449				ATTORNEY DOCKET NO. <b>AUS920030488US1</b>	SERIAL NO. <b>10/757,250</b>
				<b>APPLICANT Levine et al.</b>	
				FILING DATE <b>January 14, 2004</b>	GROUP ART UNIT <b>2192</b>
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
/C.K./ AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Counting Instruction Execution and Data Accesses</b>		Sep. 30, 2003
/C.K./ AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Selectively Counting Instructions and Data Accesses</b>		Sep. 30, 2003
/C.K./ AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations</b>		Sep. 30, 2003
/C.K./ AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold</b>		Sep. 30, 2003
/C.K./ AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations</b>		Sep. 30, 2003
/C.K./ AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Debug Support for Individual Instructions and Memory Locations</b>		Sep. 30, 2003
/C.K./ AG	10/675,721 / AUS920030483US1	Levine et al.	<b>Method and Apparatus to Autonomically Select Instructions for Selective Counting</b>		Sep. 30, 2003
/C.K./ AH	10/674,642 / AUS920030484US1	Levine et al.	<b>Method and Apparatus to Autonomically Count Instruction Execution for Applications</b>		Sep. 30, 2003
/C.K./ AI	10/674,606 / AUS920030485US1	Levine et al.	<b>Method and Apparatus to Autonomically Take an Execution on Specified Instructions</b>		Sep. 30, 2003
/C.K./ AJ	10/675,783 / AUS920030486US1	Levine et al.	<b>Method and Apparatus to Autonomically Profile Applications</b>		Sep. 30, 2003
/C.K./ AK	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Counting Instruction and Memory Location Ranges</b>		Sep. 30, 2003
/C.K./ AL	10/757,248 / AUS920030544US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Counting Instruction Execution and Data Accesses to Identify Hot Spots</b>		Jan. 14, 2004
/C.K./ AM	10/757,269 / AUS920030549US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Autonomically Initiating Measurement of Secondary Metrics Based on Hardware Counter Values for Primary Metrics</b>		Jan. 14, 2004
/C.K./ AN	10/757,212 / AUS920030553US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Generating Interrupts Based on Arithmetic Combinations of Performance Counter Values</b>		Jan. 14, 2004
/C.K./ AO	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	<b>Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information</b>		Jan. 14, 2004
DATE CONSIDERED 05/21/2008			EXAMINER	/Chuck Kendall/	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					